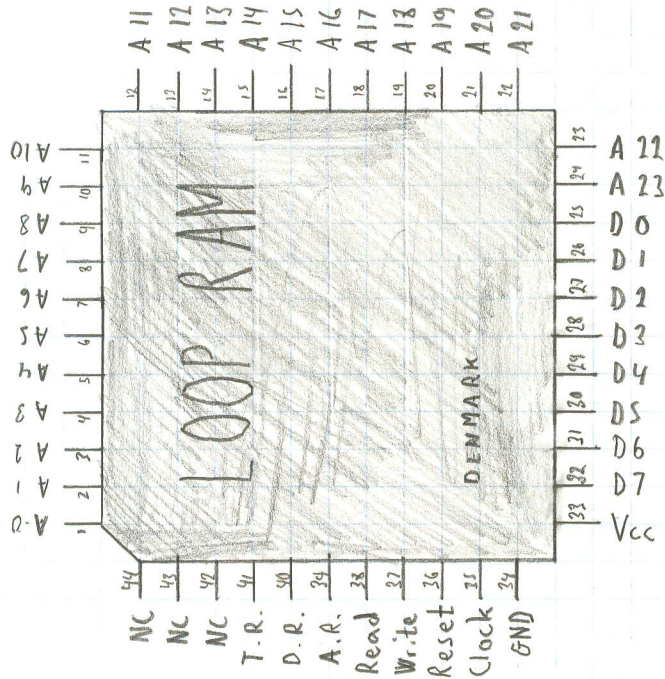


Design of Loop RAM kreds Bilag 9



Allan Krogh Jensen

8/2-1996