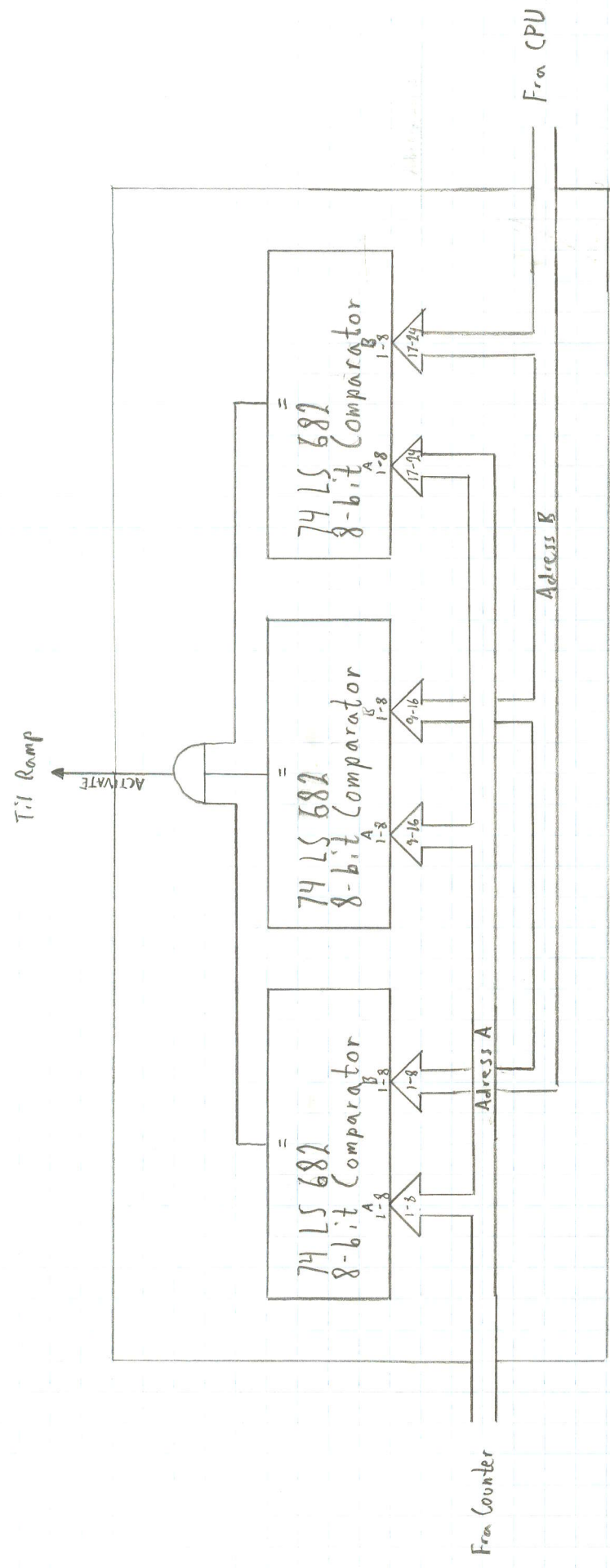


Block diagram for 24-bit Parallel Comparator Bilag 5



Allan Krogh Jensen
3/2-1996