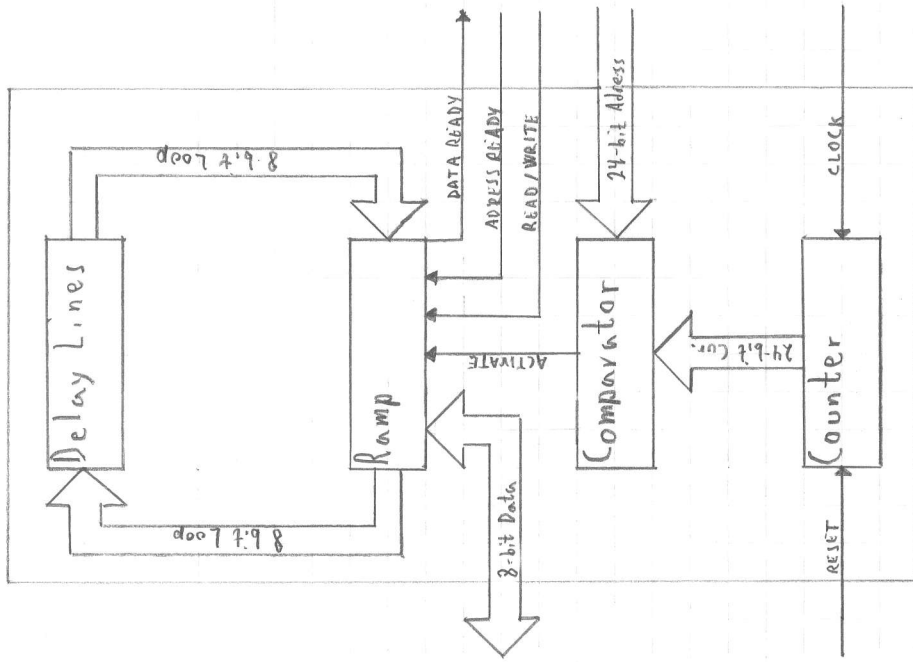


Blockdiagram for Loop RAM Bilag 3



Allan Krogh Jensen

3/2-1996